GRID FREE EULER FLOW SOLVER WITH CUDA COMPUTING

P.V.R.R. Bhogendra Rao, K. Anandhanarayanan, R. Krishnamurthy, Debasis Chakraborty Defence Research and Development Laboratory (DRDL) Kanchanbagh Post, Hyderabad-500 058 Email : debasis_cfd@drdl.drdo.in; <u>debasis_drdl@yahoo.co.in</u>

Abstract

Graphics Accelerators are increasingly used for general purpose high performance computing applications as they provide a low cost solution to high performance computing requirements. Intel also came out with a performance accelerator that offers a similar solution. However, the existing application software needs to be restructured to suit to the accelerator paradigm. Master-slave software architecture has been employed to enable two-dimensional and threedimensional grid-free Euler flow solvers in GPGPU computing platforms. Results showing significant improvement in the performance are presented in this paper. Convergence histories and aerodynamic forces obtained from GPGPU computing are compared with that of sequential computing results.

Keywords: Grid-free Solver, MPI, Cluster Computing, GPGPU, CUDA